

# Signal Audible Module

## FEATURES

- **Four Independently Programmable Indicating Circuits**
- **NFPA Style "Y" or Style "Z" Wiring**
- **24 Vdc Power Limited Outputs**
- **Self-Restoring Output Design**
- **Four Optional Coded Outputs per Circuit**

## DESCRIPTION

The Kidde® PEGAsys™ Signal Audible Module provides four programmable indicating circuits for use with UL Listed 24 Vdc polarized audible/visual indicating appliances. Each output circuit is independently programmable allowing each to annunciate separate alarm events.

Outputs are UL Listed for power limited applications. The advanced circuitry design uses resettable fuses, eliminating the need to replace modules or components if the circuits are shorted or overloaded. As a result, down time and maintenance costs are reduced.

The Signal Audible Module is capable of supporting NFPA Style "Y" and/or NFPA Style "Z" field wiring. Each output circuit is completely supervised for open and short circuit conditions. Each output operates in the reverse polarity fashion when activated. Each output delivers up to 2 Amps of 24 Vdc power which is substantial enough to meet the requirements of today's ADA devices.

The Signal Audible Module connects into the optional I/O motherboard of the PEGAsys and is supervised for proper placement by the Central Control Module. Removal of the module will result in a trouble condition.

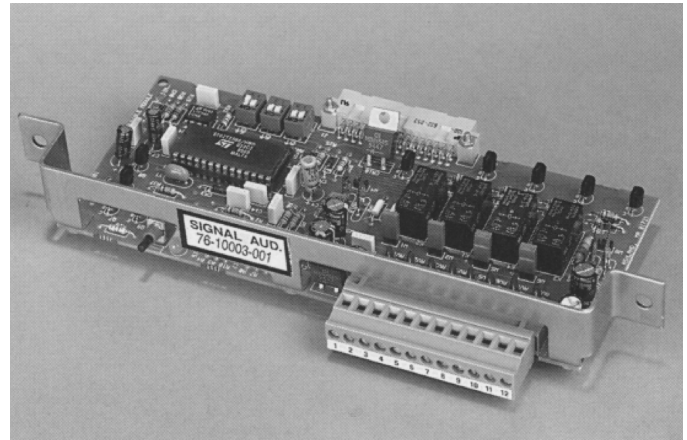
Each circuit can be separately programmed for one of the following coded outputs:

1. Continuous Sounding
2. 60 Beats per Minute
3. 120 Beats per Minute
4. Temporal Pattern

Utilizing the unique FailSoft™ design feature, the PEGAsys can activate a signal output even if the system experiences a failure in its main or peripheral CPUs.

## TECHNICAL INFORMATION

Rated Output: 2 Amp at 24 Vdc per circuit  
Operating Temperature: 32° to 120°F



## ORDERING INFORMATION

Part Number	Description	Weight
76-100003-001	Signal Audible Module	1 lb.
06-129520-001	EOL Resistors (Package of 50)	1 lb.

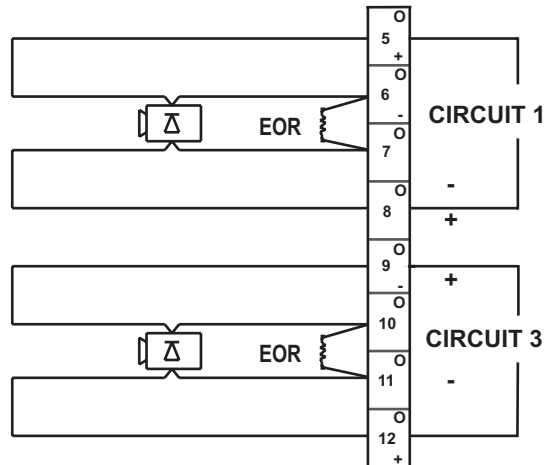


Figure 1. Style "Z" Wiring

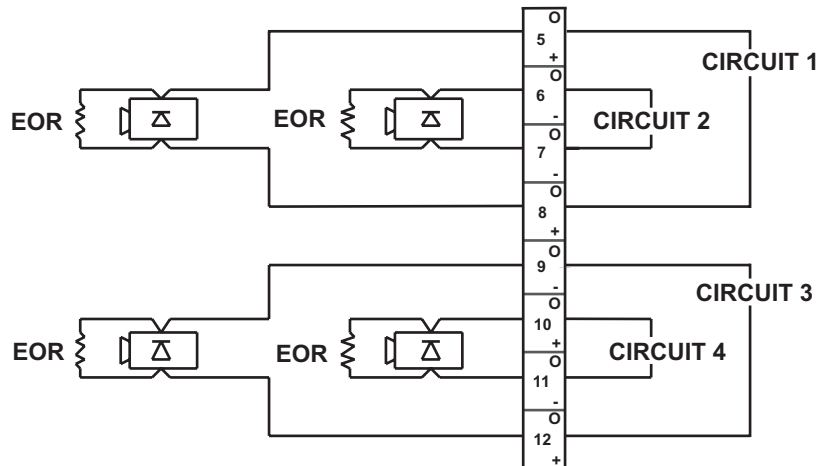


Figure 2. Style "Y" Wiring

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