ASCII Keyboards Parallel & Serial Output

INSTRUCTION & REPAIR MANUAL



Features:

- Serial & Parallel In One Unit
- Self Test Feature
- Eight Baud Rates
- N-Key Rollover
- Auto Repeat
- **■** Customizable For Volume Orders
- KB670A/KB675A CMOS Version



Key/Output Character Table

Keyboard	0	0	0	1 Control	Keyboard	0	0	1/	Control
Key	0	1	x	X Shift	Key	0	1	x	Shift
	·	x	#	X U&L Case		×	x	х	U&L Case
Α	a	Α	Α	soн	0	0	0	NUL	
В	b	В	В	STX	1	1	!	NUL	
С	С	С	C	ETX	2	2	"	NUL	
D	đ	D	D	EOT	3	3	#	NUL	
E	e	E	E	ENQ	4	4	\$	NUL	
F	f	F	F	ACK	5	5	%	NUL	
G	g	G	G	BEL	6	6	8	NUL	
Н	h	Н	Н	BS	7	7		NUL	1
1	l i	1	1	нт	8	8	1	NUL	1
J	l i	J	J	LF	9	9	1;	NUL	
K	k	ĸ	K	VT	-	_	'	NUL	
L	1	L	1 L	FF	1.		١.	NUL	ļ
M	m	М	м	CR	į.	. .	+	NUL	
N	n	N	N	so	['	'	<	FS	
0	0	o	0	SI	'_	' '	=	GS	
P	p	P	P	DLE			>	RS	
Q.	q	a	Q	DC1	1	,	1	US	
R	r	R	R	DC2	'	'	-	03	
s	s	s	s	DC3	r	r	1	ESC	
ī	t	T	T	DC4	1		11	NUL	
Ü l	ù	Ú	ان	NAK	\ <u>`</u>	\ \	1	NUL	
v	v	V	V	SYN	11	,	1		
w	-	ĺ w	ľw	1	DEL	^	~	NUL	İ
X	w	X		ETB	l i	DEL	ļ ,	DEL	
	X		X	CAN	@	@		NUL	1
Y	y	Y	Y	EM					
z	Z	Z	Z	SUB	SP	SP	.SP	SP	
1 - Key Pre		CR	CR	CR	CR				
0 - Key No		LF +	LF	LF	LF				
X - Don't Care						+	+	+	
* - Pin 12 Connected to Ground, Serial Port						*	*	*	
# - Pin 12 (=	=	=	=				
			nd, Paralle	l Port	ESC	ESC	ESC	ESC	ļ
# - Pin 4 O	pen, Para	allel Port			BS	BS	BS	BS	1

Character/ASCII Code Table: ASCII (Without Parity)

	0	1	2	3	4	5	6	7	8	9	Α	В	С	D	E	F
0	NUL	SOH	STX	ETX	EOT	ENQ	ACK	BEL	BS	нт	LF	VT	FF	CR	so	SI
1	DLE	DC1	DC2	DC3	DC4	NAK	SYN	ЕТВ	CAN	EM	SUB	ESC	FS	GS	RS	us
2	SP	!	"	#	\$ ·	%	&	/	()	•	+		-		1
3	0	1	2	3	4	5	6	7	8	9	:	;	<]=	>	?
4	@	Α	В	С	D	Ε	F	G	н	ı	J	к	L	М	N	0
5	Р	a	R	s	Т	U	٧	w	х	Υ	Z	ı]	V	_
6	,	а	b	С	d	e	f	g	h	i	j	k	1	m	n	0
7	р	q	r	s	t	u	y	w	×	у	z	{	;	}	.~	DEL

Examples: A is Code 41. Carriage Return (CR) is Code 0D

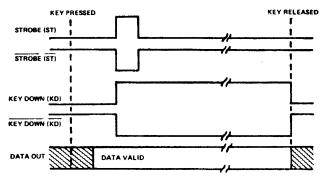
Caution: To prevent fire & shock hazard, provide power only from an isolated, UL Approved, Class 2 power supply rated 5 VDC, 250 mA minimum (and \pm 15 volts at 40 mA minimum if RS232C is implemented). Do not exceed voltage and current ratings of the USER1/USER2 keys.

Not for use in explosive atmosphere.

Contains static sensitive devices.

Parallel Output

Timing Diagram



NOTE: STROBE AND STROBE PULSE APPROX. 3.2

KEY DOWN AND KEY DOWN DELAYED FROM STROBE AND STROBE LEADING EDGE BY APPROX. 1.6

STROBE LEADING EDGE BY APPROX. 1.6

#S.

Connector - Rear View (P5)



No. 3421-8020 OR EQUIVALENT

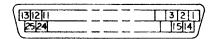
CAUTION: Inserting cable connector with power applied may cause permanent damage.

Serial Output

Data Format



Connector - Rear View (P4)



MATING CONNECTOR: 25-PIN MALE "D" CABLE CONNECTOR

CAUTION: Inserting cable connector with power applied may cause permanent damage.

Serial Output Configuration Switches

Configuration Control Switches

SWITCH NUMBER								
1	2 .	3	4	5	6			
1	0	0	0	0	0			
0	1	0	0	0	0			
1	1	0	0 .	0	0			
0	0	1	0	0	0			
1	0	1	0	0	0			
0	0	0	1	0	0			
0	0	0	0	1	0			
0	0	0	0	0	1			
	1 1 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1 2 1 0 0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	1 2 3 1 0 0 0 1 0 1 1 0 0 0 1 1 1 0 0 0 1 1 0 0 0 0 0 0 0 0	1	1			

^{*}Note: 8th Bit is zero if 8 Bit word length is selected.

SWITCH NUMBER

7	8	9	10]
1	×	Х	×	Even Parity
0	х	х	×	Odd Parity
×	1	х	х	8 Bit Word*
×	0	х	×	7 Bit Word**
×	х	1	×	2 Stop Bits
X	х	0	×	1 Stop Bit
×	×	×	1	Inhibit Parity
X	х	Х	0	Enable Parity

^{1 -} Switch Closed (Down)

^{**} Note: A switch setting of 7 data bits, no parity and one stop bit is an invalid setting and will generate a ten bit word with two stop bits.

^{0 -} Switch Open (Up)

X - May be up or down dependant on desired configuration

Disassembly Procedure

Should disassembly be required, the following procedure should be followed:

- 1. Be sure power on all connecting cables has been turned off.
- 2. Disconnect interconnecting cables.
- 3. Remove three screws from each end cap, six screws total.
- 4. Remove end caps by pulling away from keyboard end.
- 5. Slide the bottom panel toward the left or right end of the keyboard.
- 6. With the keypad face down on a flat surface, remove the screws at the front edge of the printed circuit board. Having removed the screws, the front keyboard metal lip will drop away from the assembly. If it does not, gently pull this front metal piece from the assembly.
- 7. Lift the front edge of the printed circuit board up slightly and very carefully remove the keypad tails from the connectors located at the front of the printed circuit board.
- 8. Pull the printed circuit board forward far enough that the rear connector clears the rear panel.

Disassembly is now complete.

CAUTION: Do not attempt to remove the keypad from the metal surface - the keypad will be destroyed!

TO ASSEMBLE: Reverse this procedure.

Circuit Operation Description

The KB670/KB670A and KB675/KB675A keyboards are designed around a high speed, multiport microcontroller, U4(8051 family). This device is supported by a precision crystal oscillator Y1, baud rate switches SW1 thru SW6, parity and word length switches SW7 thru SW10 and a power-on reset circuit, U7.

The controller scans the baud rate and parity switches to determine the user's requirements before each scan of the keyboard. Baud rate switches are read via the multiplexer, U6. Setting of an invalid baud rate will result in a short repetitive tone indicating the need for correction. Keys pressed are stored by the controller in a queue and compared to the keys pressed on a subsequent scan. Occurrence of the same key on two successive scans validates the key for transmission.

Keypresses are transmitted both serially and as parallel bytes with parity. Serial data is buffered via U5 before driving the TTL data output, the current loop circuit or the RS232 driver. The current loop circuit is composed of Q3, R16, R17, R18, R21, R22, R23, D3, D4, and U2. This circuit is passive and bipolar, the user does not need to be concerned about the current source polarity. The RS232 driver is U3. Parallel data is latched into U1 and captured externally via the strobe or strobe-not signals available to the user. All keypresses are further indicated externally via the keydown-not signals which are active for the duration of the keypress. These latter four signals are also buffered by U5.

An audible tone is generated during keypresses via the speaker driver circuitry composed of R10 thru R13, R19, R20, C9, Q1, and D1.

The prompt LED, D2, driven by Q2, is normally on indicating the power is on. The user may turn the LED off via the prompt input.

This product contains a self test feature invoked by pulling pin 25 of the RS232 connector low. A check sum of the ASCII Character Code Table is performed and all ASCII characters from 20H to 7EH are output on both the serial and parallel ports at the baud rate selected. Any error detected in the check sum will beep the speaker at a 1 second rate and no ASCII codes will be output.

Replacement Guide

KB670 Replaces RCA Keyboards VP601 and VP606 KB675 Replaces RCA Keyboards VP611 and VP616

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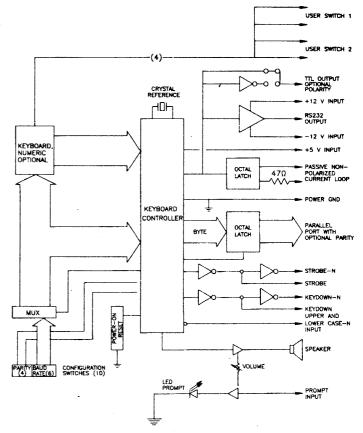
Acceptance Of Goods

Notwithstanding any items or conditions which may appear on the customer's order, this article is offered for sale specifically on the above condition, and may not be accepted and retained by the purchaser except upon that condition.

Notice: Radio Frequency Interference Statement

The equipment described in this manual generates and uses radio frequency energy. If it is not installed and used properly, that is, in strict accordance with the manufacturer's instruction, it may cause interference to radio and television reception.

This equipment has been tested and complies with the limits for a Class B digital device in accordance with the specifications in Part 15 of the FCC Rules. These rules are designed to provide reasonable protection against such interference in a residential installation. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause interference to radio or television reception, which can be determined by turning the equipment on and off, the user is encouraged to try to correct the interference by one or more of the following measures:



* Reorient the receiving antenna * Relocate the computer with respect to the receiver * Move the computer away from the receiver * Plug the computer into a different outlet so that computer and receiver are on different branch circuits

If necessary, the user should consult the dealer or an experienced radio/television technician for additional suggestions. The user may find the following booklet, prepared by the Federal Communications Commission, helpful: "How to Identify and Resolve Radio and TV Interference Problems". This booklet is available from the U.S. Government Printing Office, Washington, DC 20402.

WARNING

This equipment has been certified to comply with the limits for a Class B digital device under Part 15 of FCC Rules. Only peripherals (computer input/output devices, terminals, printers, etc.) certified to comply with the Class B limits may be attached to this computer. Operation with non-certified peripherals is likely to result in interference to radio and TV reception. Any changes or modifications to this equipment not expressly approved by the manufacturer could void the user's authority to operate this equipment.

REMARKS

To meet FCC requirements, shielded interface cables are required to connect the device to a personal computer, peripheral, or other Class B certified device.

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Matric Limited Hill City Road, RR1 Seneca, PA 16346, USA Printed in U.S.A./11-93 KB670, KB675

Design Specifications

Character Set:

128 character ASCII (102 character alphabetical upper-case-only selectable by application of logic level to a connector pin).

Keyboard:

KB670/KB670A - 58-key typewriter format. Includes two user definable keyboard switch closures.

KB675/KB675A - Same as KB670 plus a 16-key, calculator format numeric keypad.

Rollover:

N-key rollover.

Auto Repeat:

An alpha-numeric key held for approx. 1 sec. will auto repeat at the rate of 10 characters a second.

User Keys:

USER 1, USER 2 - SPST switch closures. Rating - 30 V. 0.1 A, 1 W max.

Audio

1 kHz tone during keypress. Short duration chirp tone to verify SHIFT or CONTROL keypress. Adjustable volume.

Power Required: (the +5 VDC tolerance is ± 5% at keyboard)
KB670/KB675 - +5 VDC @ 180 mA for parallel, TTL serial and 20 mA
current loop. Additional ± 9 VDC to ± 15 VDC @ 5 mA
for RS232C compatible output.

KB670A/KB675A - +5 VDC @ 105 mA for parallel, TTL serial and 20 mA current loop. Additional ± 9 VDC to ± 15 VDC @ 5 mA for RS232C compatible output.

NOTE:

+5 VDC current requirement can be reduced up to 50 mA for KB670, KB675, KB670A and KB675A by adjusting the speaker volume to minimum output level.

Outputs:

Parallel - 7 bit ASCII; even, odd or no parity. Strobe and keydown handshaking. Will drive 10 TTL loads.

Serial - Selectable 7 or 8 bits; even, odd or no parity; 1 or 2 stop bits; 110, 300, 600, 1200, 2400, 4800, 9600 or 19.2K baud data rates; TTL(ten load drive),20 mA current loop(110, 300 and 1200 baud) and RS232C compatible serial outputs.

LED Indicator:

Green, Power on or can be used as a host controlled prompt.

Connectors:

Parallel - 20 pin right angle, PC board header connector. Mates with 3M 3421-6020 ribbon cable connector or equivalent.

Serial - 25 pin subminiature D connector.

Size and Shipping Weight (Approx.):

KB670/KB670A - 13.1x7x2 inches (333x178x51 mm), 3 lbs (1.4 kg) KB675/KB675A - 16.5x7x2 inches (419x178x51 mm), 3.5 lbs (1.6 kg)

-- KB675 Only -

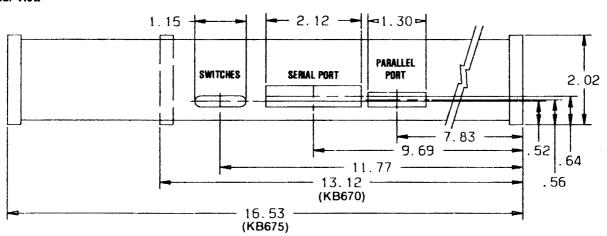
Environmental:

Temperature - 0° to +50° C, operating. - 40° to +85° C, storage.

Keyboard Layout



Rear View



Dimensions in inches