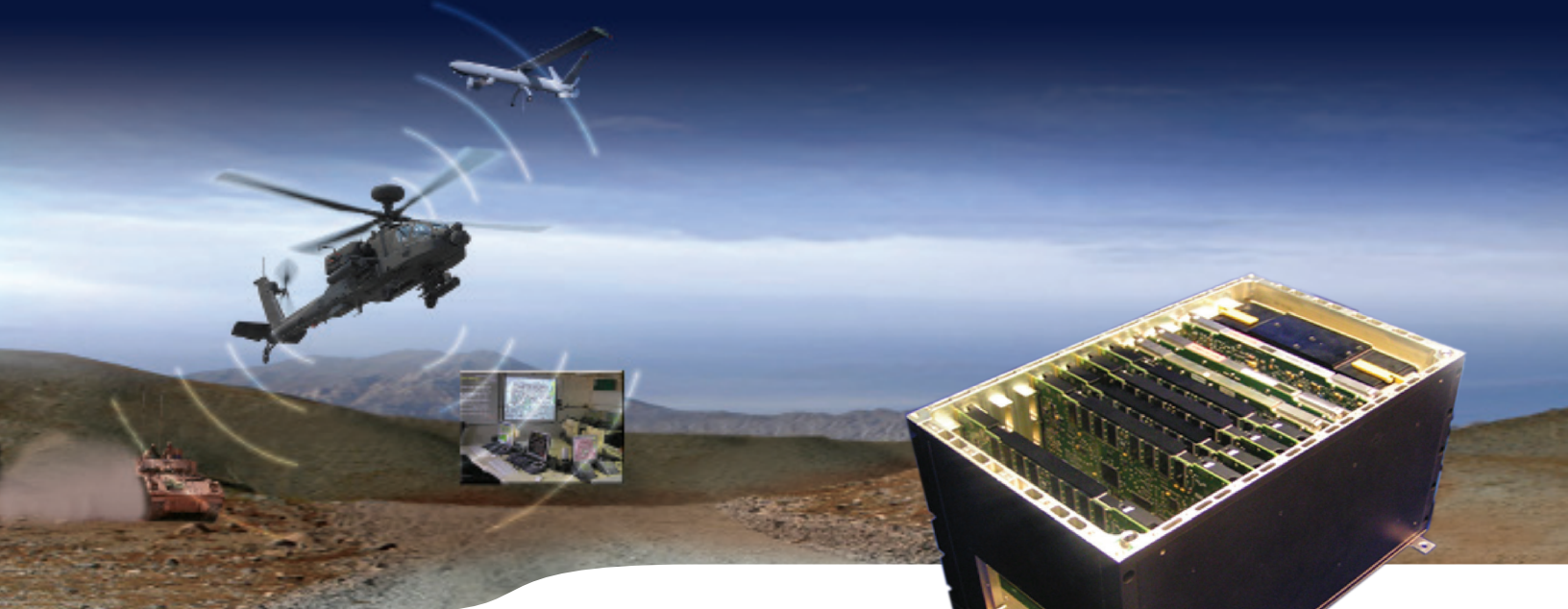


# Central Mission Processor (CMP)/ Block III Mission Processor (MP)



The central mission processor is the first configuration of advanced processors that Elbit Systems of America will provide in support of the Boeing Apache program. A full performance Block III mission processor has been delivered to Boeing and is the cornerstone for the Apache AH-64E program.

With up to 25 general purpose processors and four graphics processors, the mission processor will be the cornerstone for Block III Apache and provides the basis for implementation of SoSCOE and FCS for the Apache. A true open systems architecture is achieved with the leverage of commercial, off-the-shelf products that provides Army aviation with a high performance, scalable, and maintainable for future applications that require the full performance necessary for next generation computing capabilities. The central mission processor/mission processor architecture is scaleable and suited for multiple platforms, providing advanced general and display processing for current and future needs where SoSCOE and FCS is planned.

# Central Mission Processor (CMP)/ Block III Mission Processor (MP)

## Features

- Implements true open systems architecture with no proprietary interfaces
- Fully supports software reuse
- Meets long-term Block III requirements
- 25 PowerPC processors
- Four graphics processors
- Future combat system ready
- Combines:
  - Commercial off-the-shelf (COTS)
  - Variant COTS
  - Custom CCAs

Elbit Systems of America is committed to implementing the Boeing/US Army vision of a high performance, scalable, and maintainable mission processor capability.

Elbit systems of America has successfully implemented OSA and COTS-like systems for several platforms – providing experience and reducing development risk.



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