

Verification Engineer, Full-Time, Bristol

Graphcore has created a completely new processor, the Intelligence Processing Unit (IPU), specifically designed for artificial intelligence. The IPU's unique architecture means developers can run current machine learning models orders of magnitude faster. More importantly, it lets AI researchers undertake entirely new types of work, not possible using current technologies, to drive the next great breakthroughs in general machine intelligence.

We believe our IPU technology will become the worldwide standard for artificial intelligence compute. The performance of Graphcore's IPU is going to be transformative across all industries and sectors whether you are a medical researcher, roboticist or building autonomous cars.

Our team is at the forefront of the artificial intelligence revolution, enabling innovators from all industries and sectors to expand human potential with technology. What we do, really makes a difference.

As a Verification Engineer at Graphcore you will be responsible for ensuring that our IPU performs in accordance with its specification and that its implementation is similarly accurate. You will be familiar with modern CPU and ASIC verification methodologies including, but not limited to, constrained random test generation and functional coverage.

The Verification team at Graphcore interacts closely with architects, RTL designers and software tool chain engineers. You will be fluent in their languages and work efficiently with them to determine the root causes of complex issues. You will be able to describe these issues accurately and concisely when filing bug reports.

You will be comfortable with working at scale. Not only logically within our IPU but also keeping our large simulation farm busy with a continuous flow of verification tasks prospecting for issues and closing coverage.

Finally you will add value to our verification infrastructure by adding useful features and improving throughput.

Responsibilities

- Verification planning, specification of functional coverage
- Providing feedback to architects
- Test generation and failure diagnosis/triage
- Contributing to shared verification infrastructure

Key Skills

- **Must have**
 - Be highly motivated, a self starter, and a team player
 - Ability to work across teams and programming languages to find root causes of deep and complex issues

- Experience of the verification process applied in CPU and/or ASIC environments
- System Verilog, Python, C++, Linux
- **Some of**
 - UVM
 - SVA
 - Assembly languages
 - LLVM, GCC
 - DVCS e.g. Git
 - SGE or other DRMS
 - XML and XPath/XSLT
 - Web programming – HTML/DOM, Javascript, SQL

We welcome people of different backgrounds and experiences and are committed to building an inclusive work environment that makes Graphcore a great home for everyone. We are an equal opportunity employer and want to build a work environment where everyone is happy, productive and respectful so they can do their best work. If you have a disability or additional need that requires accommodation, just let us know.